

FIG. 1 is a schematic diagram of a system for controlling a femtosecond laser (FEMR) using a microcontroller (MCU) and a digital-to-analog converter (DAC). The system includes a power supply (PS) connected to the MCU and the DAC. The MCU is connected to the DAC and the FEMR. The DAC is connected to the FEMR. The FEMR is connected to a series of output lines (OL) which are connected to a series of output devices (OD). The output devices are connected to a series of output lines (OL) which are connected to a series of output devices (OD). The output devices are connected to a series of output lines (OL) which are connected to a series of output devices (OD).

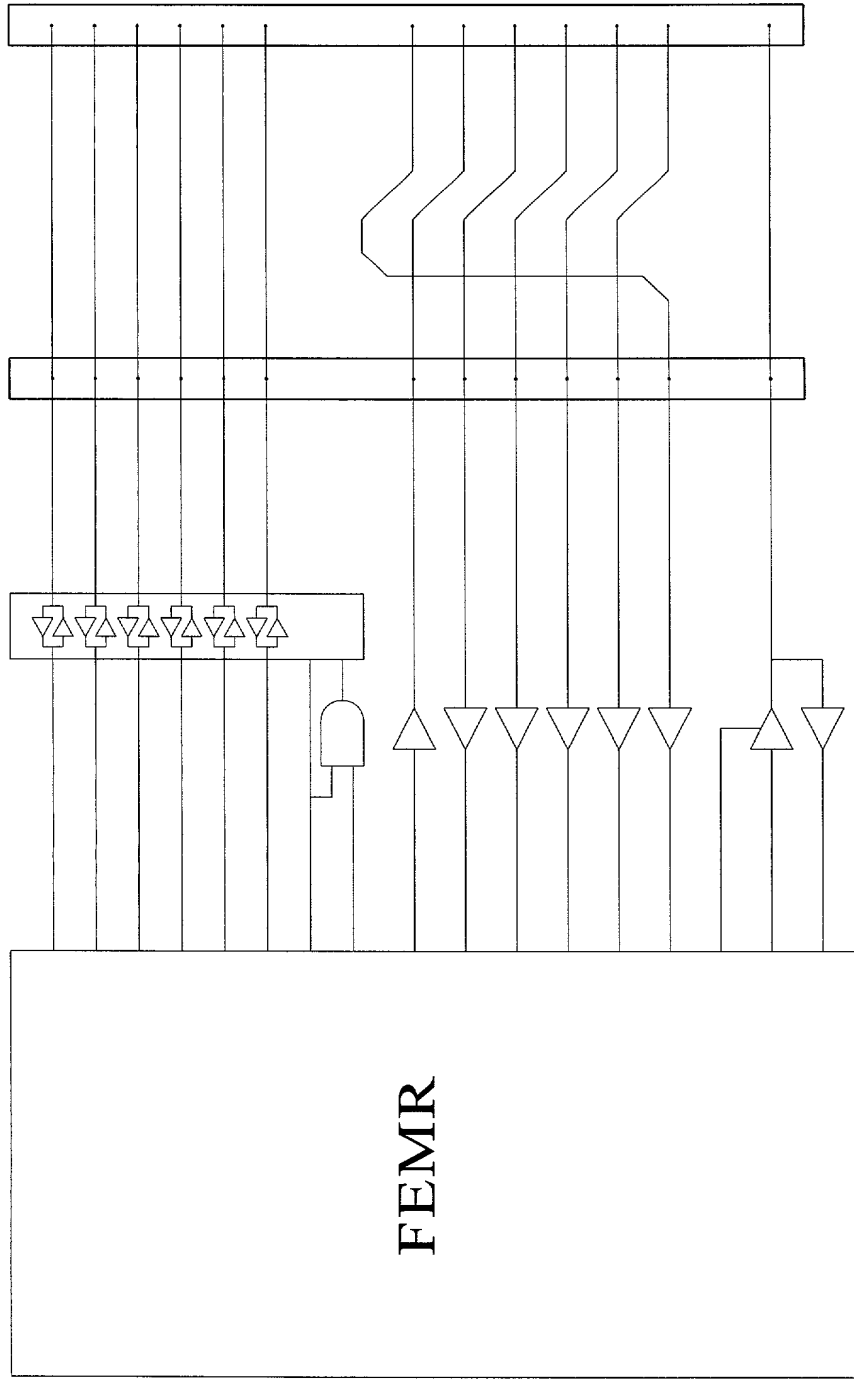


FIG. 1

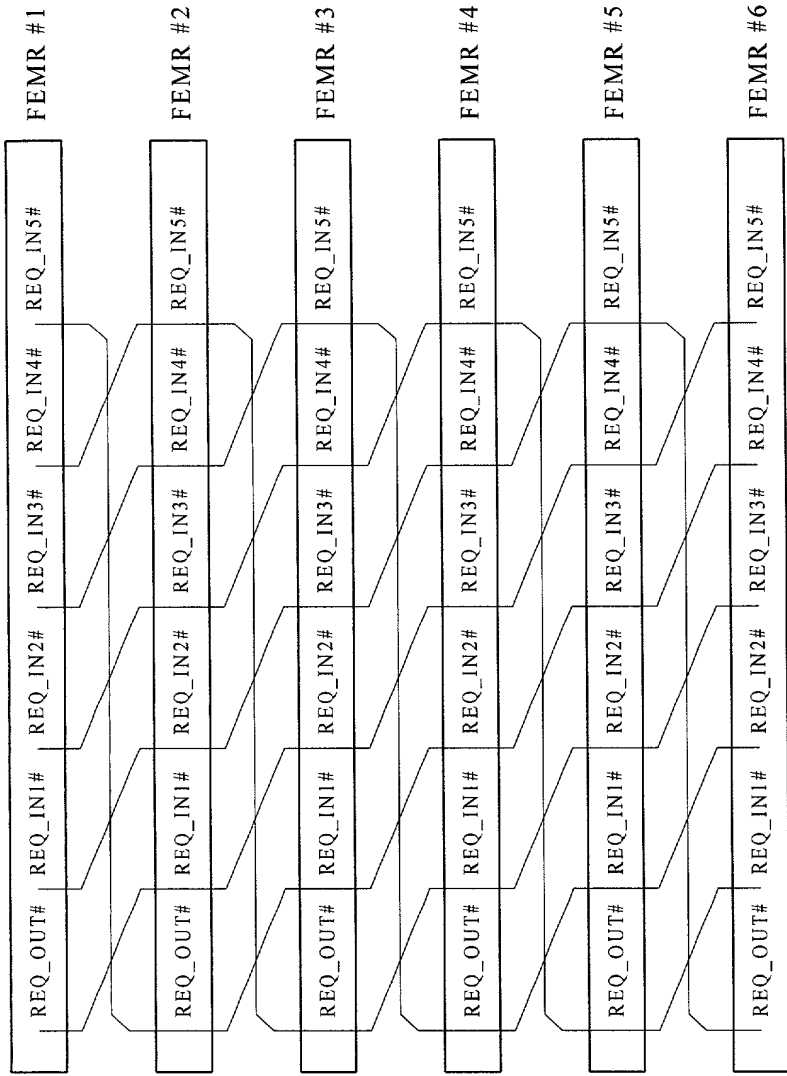


FIG. 2

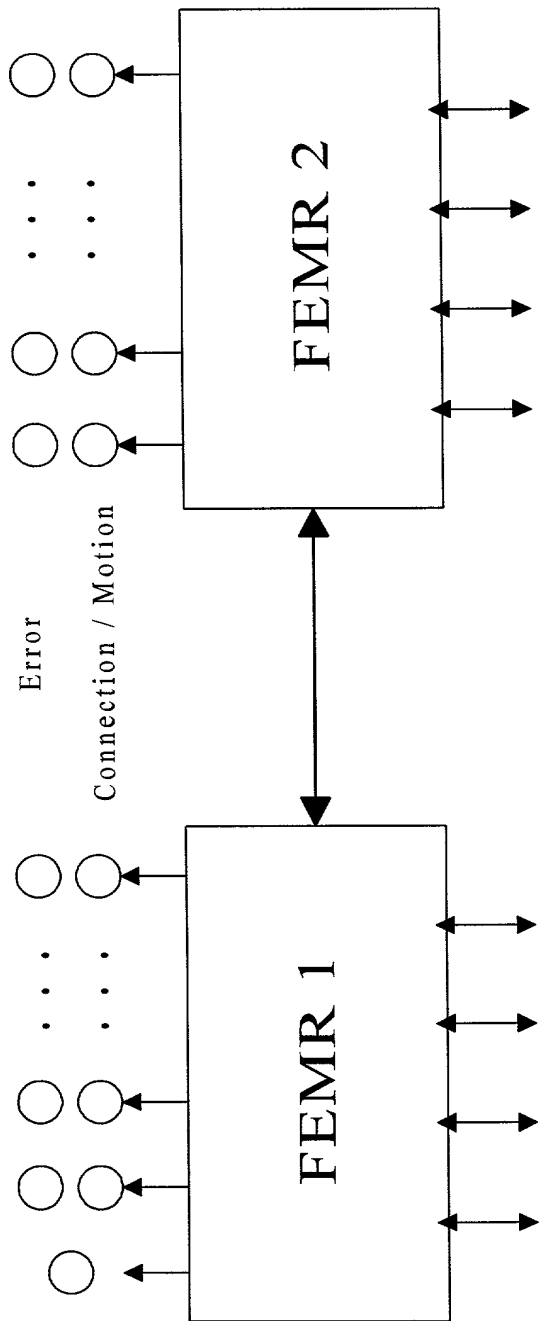


FIG. 3